

**IN THE CLAIMS**

This listing of claims will replace all prior versions, and listings, of claims in the application. An identifier indicating the status of each claim is provided.

**Listing of Claims**

1. (currently amended) A digital signal processing apparatus, comprising:
  - a plurality of digital signal processing blocks and a host arithmetic operation processing block as functions necessary for processing a digital signal;
  - a bus common bus for connecting said host arithmetic operation processing block and said plurality of digital signal processing blocks;
  - interface means coupled to said bus common bus to enable a block to be added to the bus common bus or to enable a block connected to the bus common bus to be changed; and
  - means for encrypting data of a stream transferred through said bus common bus.
2. (currently amended) The digital signal processing apparatus as set forth in claim 1, wherein said plurality of digital signal processing blocks include encrypting/decrypting means for encrypting/decrypting the data of the stream transferred through said bus common bus.
3. (original) The digital signal processing apparatus as set forth in claim 1, wherein the data of the stream contains video data and/or audio data.
4. (original) The digital signal processing apparatus as set forth in claim 3, wherein the video data and/or the audio data has been compressed.

5. (currently amended) The digital signal processing apparatus as set forth in claim 1,

wherein said common bus is a general-purpose bus, and

wherein each block connected to said bus common bus can be added or

substituted.

6. (currently amended) A digital signal processing apparatus, comprising:

a plurality of digital signal processing blocks and a host arithmetic operation

processing block as functions necessary for processing a digital signal;

a bus common bus for connecting said host arithmetic operation processing block

and said plurality of digital signal processing blocks;

interface means coupled to said bus common bus to enable a block to be added to

said bus common bus or to enable a block connected to said bus common bus to be changed; and

means for encrypting the data of the stream that is output through said interface of the extension function providing medium when the data of the stream is transferred to the extension providing medium through said bus common bus.

7. (original) The digital signal processing apparatus as set forth in claim 6, wherein said

interface of the extension function providing medium includes encrypting/decrypting means for

encrypting/decrypting data of a stream that is output through said interface of the extension

function providing medium.

8. (original) The digital signal processing apparatus as set forth in claim 6, wherein the data of the stream contains video data and/or audio data.

9. (original) The digital signal processing apparatus as set forth in claim 8, wherein the video and/or audio data has been compressed.

10. (currently amended) A digital signal processing method, comprising the steps of:  
structuring functions necessary for processing a digital signal as a plurality of digital signal processing blocks and a host arithmetic operation processing block;  
connecting the host arithmetic operation processing block and the plurality of digital signal processing blocks through ~~the bus a common bus~~;  
providing a means coupled to said ~~bus common bus~~ to enable a block to be added to said ~~bus common bus~~ or to enable a block connected to said ~~bus common bus~~ to be changed;  
and  
encrypting data of a stream transferred through the ~~bus common bus~~.

11. (currently amended) The digital signal processing method as set forth in claim 10, wherein the plurality of digital signal processing blocks include a step for encrypting/decrypting the data of the stream transferred through the ~~bus common bus~~.

12. (original) The digital signal processing method as set forth in claim 10, wherein the data of the stream contains video data and/or audio data.

13. (original) The digital signal processing method as set forth in claim 12, wherein the video data and/or the audio data has been compressed.

14. (currently amended) The digital signal processing method as set forth in claim 10,  
wherein the common bus is a general-purpose bus, and  
wherein each block connected to the bus common bus can be added or substituted.
15. (currently amended) A digital signal processing method, comprising the steps of:  
    structuring functions necessary for processing a digital signal as a plurality of  
digital signal processing blocks and a host arithmetic operation processing block;  
    connecting the host arithmetic operation processing block and the plurality of  
digital signal processing blocks through a bus common bus;  
    providing a means coupled to said bus common bus to enable a block to be added  
to said bus common bus or to enable a block connected to said bus common bus to be changed;  
and  
    encrypting the data of the stream that is output through the interface of the  
extension function providing medium when the data of the stream is transferred to the extension  
function providing medium through the bus common bus.
16. (original) The digital signal processing method as set forth in claim 15, wherein the  
interface of the extension function providing medium includes a step for encrypting/decrypting  
data of a stream that is output through the interface of the extension function providing medium.
17. (original) The digital signal processing method as set forth in claim 15, wherein the data of  
the stream contains video data and/or audio data.

18. (original) The digital signal processing method as set forth in claim 17, wherein the video data and/or the audio data has been compressed.